



Docket: 0819-0242

#4
Rec - Amt A
6/26/01
K Duncan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
In re Patent Application of)
Taiji NODA et al.)
Serial No. 09/662,358)
Filed: September 15, 2000)
For: METHOD FOR FABRICATING)
SEMICONDUCTOR DEVICE)

Attention: Applications Branch

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with
The United States Postal Service with sufficient postage as First
Class Mail in an envelope addressed to: Assistant Commissioner
for Patents, Washington, D.C. 20131, on 12/19/00

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Please preliminary amend the subject application as follows:

In the Claims:

Insert
a1

Please add new claims 16-19:

16. A method for fabricating a semiconductor device according to claim 1, wherein
the heavy ions are indium ions.

17. A method for fabricating a semiconductor device according to claim 5, wherein
the heavy ions are indium ions.

18. A method for fabricating a semiconductor device according to claim 9, wherein
the heavy ions are indium ions.

19. A method for fabricating a semiconductor device according to claim 13,
wherein the heavy ions are indium ions.

REMARKS

Claims 1-15 are pending in this application. New claims 16-19 have been added.
Examination on the merits is requested.

Respectfully submitted,



Eric J. Robinson
Reg. No. 38,285

NIXON PEABODY LLP
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102
(703) 790-9110